MoSys Newsletter
Accelerating Data Intelligence
www.mosys.com
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A Message from MoSys
News From Our CEO

Welcome to another edition of the MoSys newsletter. In this edition we are focusing on Arrow Electronics. In January of this year, MoSys announced an agreement that enables Arrow Electronics, Inc., the world's leading electronics component distributor, to sell MoSys' QPR Memory /Bandwidth Engine memory ICs and Stellar Packet Classification IP that accelerate Intel and Xilinx FPGAs and also our LineSpeed family of 100G PHY ICs. We've hit the ground running and have been hard at work bringing our solutions to the talented Arrow team so they can help our customers find the best acceleration solutions on the market.

Arrow is helping spread the word that MoSys offerings eliminate device bottlenecks while delivering speed and intelligence for a diverse number of applications ranging from Smart NICs, security, data acquisition, networking and search, Aerospace & Defense, video and high-speed Test & Measurement systems. MoSys solutions work with any high-speed FPGA to improve performance and free up valuable FPGA resources while reducing system-level power, board real estate, pin count and time to market.

Arrow has formidable resources including:

- Field Sales and Application engineers in all markets
- Centralized applications support in Denver, CO headquarters
- Industry-leading asset management to insure product availability
- Robust eCommerce capabilities via arrow.com

We hope you find this newsletter informative. Please feel free to provide feedback on the content or any new ideas that you might have.

Dan Lewis
CEO, MoSys, Inc.

News Alerts!

MoSys Announces Optimized P4 Pipeline Support for Stellar Packet Classification Platform IP for FPGAs
May 17, 2021

MoSys Expands Patent Portfolio with Purchase of Custom Algorithm Search Patents
April 12, 2021

MoSys Announces New, Low Price Point for Its LineSpeed™ Flex 100G PHY IC Product Family
Feb. 23, 2021

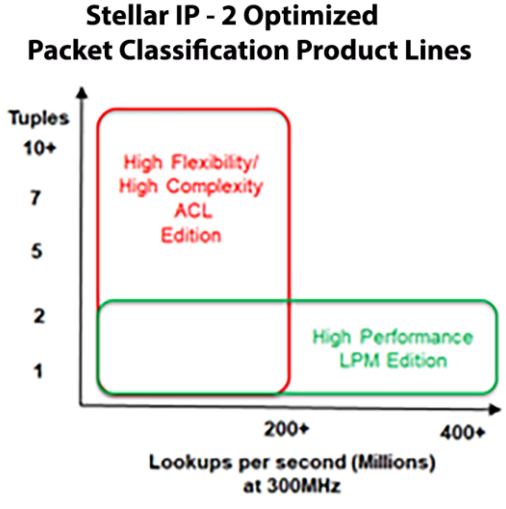
MoSys and Arrow Electronics Collaborate to Optimize System Memory on FPGA Designs
Jan. 11, 2021

Multiplexing and Demultiplexing High-Speed Serial Links with MoSys LineSpeed™ Flex PHY
Oct. 22, 2020

100G Gearbox with RS-FEC Solution for QSFP28-Based Optics with the MoSys LineSpeed™ Flex PHY IC
Sept. 30, 2020

Featured Design & Technical Documents:

- Shooting Straight with Arrow Electronics
- High-Speed Board Design Guidelines



MoSys Design Support Center

Featured Use Cases

Packet Classification in Cloud and Enterprise Datacenters Keeping Up with 4X the LPM Routing Demand

The latest information publicly available puts the number of IPV4 routes at close to a million routes with potentially more than 4 billion connected devices using IPV4. IPV6 routes are still relatively small – less than 300 thousand – but with the advent of 5G cellular wireless and IoT (Internet of things) devices, that number is expected to increase dramatically over the next few years.

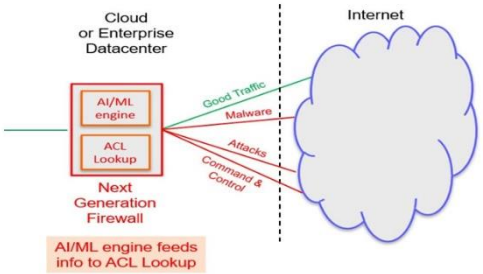
The MoSys Stellar Packet Classification Platform High Performance LPM Edition uses a HW accelerated Algorithmic TCAM-like approach to help ensure that a router can keep up with the huge volume of routing decisions that it has to process.

Read more: LINK Download the Use Case

Packet Classification for Next Generation Network Firewalls Combining AI/ML Engines with ACL Lookups

Network Firewall complexity has increased dramatically in the last decade. They now often include some form of Artificial Intelligence or Machine Learning Algorithms to spot malware infestations, but typically the first and last stages use Access Control List lookups which can become a bottleneck.

The MoSys Stellar Packet Classification Platform High Flexibility/High Complexity ACL & LPM Edition also uses a HW accelerated Algorithmic TCAM-like approach to help ensure that Network Firewalls can keep up with the huge volume of access control decisions that it has to process per second. Read more: LINK
Download the Use Case



Redundant Link Mode for High-Rel Data Transfer

Reliable data transfer is critical in many applications to ensure data movement through the system.

Redundant links:

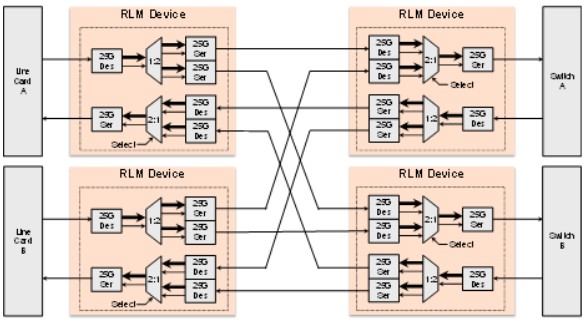
- Prevent critical system shut down
- Are critical where time sensitive data can cause a safety issue or financial loss.
- Help ensure transient data which cannot be stored or retransmitted is not lost, such as in high-speed data acquisition

The MoSys Solution:

The MoSys MSH420 device is the best fit for redundant systems.

- Supports critical industry standards, such as:
- IEEE and OIF 10G, 25G, 40G and 100G standards
- Protocol independent payload supports Datacom, Telecom, Storage applications
- Forward Error Correction (FEC) payload support allows direct connection to 25G and 100G optical standards requiring RS-FEC (e.g. SR/SR4, CWDM, PSM4)
- Devices in volume at less than $50 each

Read more: LINK Download the Use Case



Collateral: LineSpeed Flex Overview
MSH221 Product Brief MSH222 Product Brief
MSH225 Product Brief MSH322 Product Brief
MSH320 Product Brief MSH321 Product Brief
Solution Note: Comparison of Bandwidth Engine to Other Memory Solutions
STELLAR Product Brief

Email us and we will arrange to have one of our technical specialists speak with you. You can also sign up for updates. Finally, please follow us on social media so we can keep in touch.